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PATENT

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October 5, 2005

Date

Alexandra Beggs

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Ronnie M. Harrison

Attorney Docket No.: 500128.02

Patent No. : US 6,781,419 B2

Issued : August 24, 2004

Title : METHOD AND SYSTEM FOR CONTROLLING THE DUTY CYCLE OF A CLOCK SIGNAL

**NOTIFICATION OF ERRORS**

**Certificate**

**OCT 20 2005**

**of Correction**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The following errors were noted in a review of the above-identified letters patent. One or more of these errors was inadvertently made in the original application, while the others occurred in the printing of the patent. Since the errors are of an obvious nature, a formal Certificate of Correction is not believed to be warranted at this time. Therefore, applicant requests that this notification be placed in the Patent and Trademark Office file.

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Item (57), Line 6	"capacitor with the input"	--capacitor when the input--
Column 3, Line 28	"Although, the duty cycle"	--Although the duty cycle--
Column 3, Lines 32-33	"respect to FIG. 4 magnitude of the reference voltage $V_{REF}$ .	--respect to FIG. 4. The system--

	The system"	
Column 5, Line 21	"will be"	--it will be--
Column 5, Lines 22-23	"both linear in the non-linear,"	--both linear and non-linear,--
Column 6, Line 7	"inverter 42"	--inverter 42 at--
Column 7, Line 4	"ating combination"	--ating in combination--
Column 7, Line 46	"address 1bus 22"	--address bus 22--

Respectfully submitted,

Date:

October 4, 2005

By:



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Enclosure:

Postcard